

PTO/SB/21 (08-00)

MODIFIED

# TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

213200.00072

Total Number of Pages in This Submission 25

Application Number 09/920,240

Filing Date 08/01/2001

First Named Inventor Pierte ROO

Group Art Unit 2682

Examiner Name Eugene Yun

Attorney Docket Number MP0039.CIP

## ENCLOSURES (check all that apply)

☒ Fee Transmittal Form☒ Credit Card  
Authorization☐ Amendment / Reply☐ After Final☐ Affidavits/declaration(s)☐ Extension of Time Request☐ Express Abandonment Request☒ Information Disclosure Statement with  
PTO-1449s☐ Certified Copy of Priority  
Document(s)☐ Response to Missing Parts/  
Incomplete Application☐ Response to Missing Parts  
under 37 CFR 1.52 or 1.53☐ Assignment Papers  
(for an Application)☐ Drawing(s)☐ Licensing-related Papers☐ Petition☐ Petition to Convert to a  
Provisional Application☐ Associate Power of Attorney☐ Terminal Disclaimer☐ Request for Refund☐ CD, Number of CD(s) \_\_\_\_\_☐ After Allowance Communication  
to Group☐ Appeal Communication to Board  
of Appeals and Interferences☐ Appeal Communication to Group  
(Appeal Notice, Brief, Reply Brief)☐ Proprietary Information☐ Status Letter☒ Other Enclosure(s) (please  
identify below):

COMMUNICATION

Remarks

## SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm  
or  
Individual nameAndrew J. Bateman  
Registration No.: 45,573

Signature

Date

07/08/2005

## CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on this date: 

Typed or printed name

Signature

Date



# FEE TRANSMITTAL for FY 2005

Effective 10/01/2005. Patent fees are subject to annual revision.

☐ Applicant claims small entity status. See 37 CFR 1.27

TOTAL AMOUNT OF PAYMENT (\$)**180.00**

Complete if Known

Application Number	09/920,240
Filing Date	08/01/2001
First Named Inventor	Pierte ROO
Examiner Name	Eugene Yun
Art Unit	2682
Attorney Docket No.	MP0039.CIP

METHOD OF PAYMENT (check all that apply)

☐ Check ☒ Credit card ☐ Money Order ☐ Other ☐ None

☐ Deposit Account:

Deposit Account **50-1710**  
Account Name **KATTEN MUCHIN ROSENMAN, LLP**

The Director is authorized to: (check all that apply)

☐ Charge fee(s) indicated below ☒ Credit any overpayments

☒ Charge above Deposit Account with any additional fees necessary UNDER 37 CFR 1.16 AND/OR 1.17 to maintain pendency of this application.

FEE CALCULATION

1. BASIC FILING FEE

Large Entity		Small Entity		Description	Fee Paid
Fee Code	Fee (\$)	Fee Code	Fee (\$)		
1001	300	2001	150	Utility Filing Fee	300.00
1111	500	2111	250	Utility Search Fee	500.00
1311	200	2311	100	Patent Examination Fee	200.00
SUBTOTAL (1)					(\$) <b>0.00</b>

2. EXTRA CLAIM FEES FOR UTILITY AND REISSUE

Total Claims  - 20\*\* =  X  50.00 =   
Independent  - 3\*\* =  X  200.00 =   
Multiple Dependent   360.00 =

Large Entity		Small Entity		Description	Fee Paid
Fee Code	Fee (\$)	Fee Code	Fee (\$)		
1202	50	2202	25	Claims in excess of 20	
1201	200	2201	100	Independent claims in excess of 3	
1203	360	2203	180	Multiple dependent claim, if not paid	
1204	200	2204	100	** Reissue independent claims over original patent	
1205	50	2205	25	** Reissue claims in excess of 20 and over original patent	
SUBTOTAL (2)					(\$) <b>0.00</b>

\*\* or number previously paid, if greater; For Reissues, see above

FEE CALCULATION (continued)

3. ADDITIONAL FEES

Large Entity		Small Entity		Description	Fee Paid
Fee Code	Fee \$	Fee Code	Fee \$		
1051	130	2051	65	Surcharge - late filing fee or oath	
1052	50	2052	25	Surcharge - late provisional filing fee or cover sheet	
1053	130	1053	130	Non-English specification	
1812	2,520	1812	2,520	For filing a request for <i>ex parte</i> reexamination	
1804	920*	1804	920*	Requesting publication of SIR prior to Examiner action	
1805	1,840*	1805	1,840*	Requesting publication of SIR after Examiner action	
1251	120	2251	60	Extension for reply within first month	
1252	450	2252	225	Extension for reply within second month	
1253	1,020	2253	510	Extension for reply within third month	
1254	1,590	2254	795	Extension for reply within fourth month	
1255	2,160	2255	1,080	Extension for reply within fifth month	
1401	500	2401	250	Notice of Appeal	
1402	500	2402	250	Filing a brief in support of an appeal	
1403	1,000	2403	500	Request for oral hearing	
1451	1,510	1451	1,510	Petition to institute a public use proceeding	
1452	500	2452	250	Petition to revive - unavoidable (1.17(l))	
1453	1,500	2453	750	Petition to revive - unintentional (1.17(m))	
1501	1,400	2501	700	Utility issue fee (or reissue)	
1502	800	2502	400	Design issue fee	
1503	1,100	2503	550	Plant issue fee	
1460	130	1460	130	Petitions to the Director	
1807	50	1807	50	Processing fee - provisional app (1.17(q))	
1806	180	1806	180	Submission of Information Disclosure Stmt	180.00
8021	40	8021	40	Recording each patent assignment per property (times number of properties)	
1809	790	2809	395	Filing a submission after final rejection (1.129(a))	
1814	130	2814	65	Statutory Disclaimer	
1801	790	2801	395	Request for Continued Examination (RCE)	
1802	900	1802	900	Req for expedited examination - Design App	
Other fee (specify) _____					
SUBTOTAL (3)					(\$) <b>180.00</b>

\*Reduced by Basic Filing Fee Paid

SUBMITTED BY

Name: Andrew J. Bateman

Registration No.: 45,573

Telephone: (202) 625-3547

Signature: \_\_\_\_\_

*Andrew J. Bateman*

Date: 07/12/2005



Application No. 09/920,240  
Attorney's Docket No. MP0039.CIP  
Page 1

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:	)	
Pierte ROO	)	Examiner: Eugene YUN
Application No.: 09/920,240	)	Group Art Unit: 2682
Filed: August 1, 2001	)	Confirmation No.: 4035
For: ACTIVE RESISTIVE SUMMER	)	Date: July 12, 2005
FOR A TRANSFORMER HYBRID	)	

**COMMUNICATION PURSUANT TO 37 C.F.R. 1.56 AND**  
**M.P.E.P. § 2001.06(c)**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In compliance with the requirements of 37 C.F.R. 1.56 and M.P.E.P. § 2001.06(c), the Applicant hereby notifies the Patent Office that the subject matter of the above-identified application may be involved in a litigation in the United States International Trade Commission, Investigation No. 337-TA-531.

Customer No.: 28285

Should the Examiner have any questions regarding this communication or the application in general, the Examiner is urged to contact the Applicant's attorney, Andrew J. Bateman, by telephone at (202) 625-3547. All correspondence should continue to be directed to the address given below.

Respectfully submitted,

By: Andrew J. Bateman  
Andrew J. Bateman  
Attorney for Applicant  
Registration No. 45,573

IP Docket  
Katten Muchin Rosenman LLP  
1025 Thomas Jefferson St., NW  
East Lobby, Suite 700  
Washington, DC 20007-5201  
Facsimile No.: (202) 298-7570  
Customer No.: 28285



0039.CIP

Patent

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:	)	
Pierte ROO	)	Examiner: Eugene YUN
Application No.: 09/920,240	)	Group Art Unit: 2682
Filed: August 1, 2001	)	Confirmation No.: 4035
For: ACTIVE RESISTIVE SUMMER FOR	)	Date: July 12, 2005
A TRANSFORMER HYBRID	)	

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In compliance with the duty of disclosure under 37 CFR § 1.56 and in accordance with the practice under 37 CFR §§ 1.97 and 1.98, the Examiner's attention is directed to the documents listed on the enclosed PTO-1449. It is respectfully noted that Applicants do not have copies of certain literature and non-U.S. patent documents listed on the enclosed PTO-1449 forms. Applicants will provide copies of such to the Patent Office as soon as possible. If the Examiner has not received such copies at the time of consideration of the IDS, the Examiner is respectfully requested to contact the Applicants' undersigned attorney.

In accordance with 37 CFR § 1.97(h), this Information Disclosure Statement is not to be construed as an admission that the information cited is or is considered to be material to patentability as defined in 37 CFR § 1.56(b), nor as an admission that the information constitutes prior art within the meaning of 35 USC §§ 102 and/or 103.

It is respectfully requested that the information listed on the PTO-1449 be considered by the Examiner, and that an initialed copy of the PTO-1449 be returned indicating that such information was considered.

07/13/2005 SZEWDIE1 00000147 09920240

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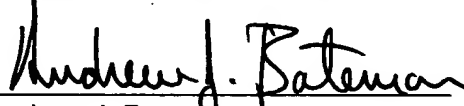
180.00 OP

Authorization for payment of the IDS fee of \$180.00 is being filed concurrently herewith.

Should the Examiner have any questions, Applicant's undersigned attorney is reachable by telephone in our Washington, D.C. office at (202) 625-3547. The correspondence address of record is provided below.

Respectfully submitted,

KATTEN MUCHIN ROSENMAN, LLP

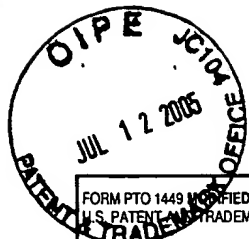
By:   
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FORM PTO 1449 MODIFIED U.S. PATENT AND TRADEMARK OFFICE		ATTORNEY DOCKET NO. MP0039.CIP		APPLICATION NO. 09/920,240			
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT Pierle ROO			
				FILING DATE 08/01/2001	GROUP 2682		
DATE SUBMITTED TO USPTO: July 12, 2005							
FOREIGN PATENT DOCUMENTS							
*EXAMINER INITIALS		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION OR ABSTRACT
OTHER DOCUMENTS (Including author, title, date, pertinent pages, etc.)							
		Hamasaki, et al., "A 3-V, 22-mV Multibit Current-Mode $\Sigma \Delta$ DAC with 100 dB Dynamic Range", December, 1996, pgs. 1888-1894.					
		Van de Plassche, "Integrated Analog-to-Digital and Digital-to-Analog Converters – Chapter 6, pgs. 211-271.					
		Lee, et al., "A CMOS Serial Link for Fully Duplexed Data Communication", April, 1995.					
		Song, et al., "FP 12.1: NRZ Timing Recovery Technique for Band-Limited Channels, 1996.					
		Song, et al., "FP 12.1: NRZ Timing Recovery Technique for Band-Limited Channels (Slide Supplement), 1996.					
		Chien, et al., "TP 12.4: A 900-MHz Local Oscillator using a DLL-based Frequency Multiplier Technique for PCS Applications".					
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		Chien, "Low-Noise Local Oscillator Design Techniques using DLL-based Frequency Multiplier for Wireless Applications", 2000.					
		Wang, et al., "A 1.2 GHz Programmable DLL-Based Frequency Multiplier for Wireless Applications", December, 2004.					
		Tsutomu Kamoto, "An 8-bit 2-ns Monolithic DAC", February, 1988.					
		Nack, et al., "A Constant Slew Rate Ethernet Line Driver", May, 2001.					
		Song, "Dual Mode Transmitter with Adaptively Controlled Slew Rate and Impedance Supporting Wide Range Data Rates", 2001.					
EXAMINER				DATE CONSIDERED			
* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							





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U.S. PATENT AND TRADEMARK OFFICE

ATTORNEY DOCKET NO.

APPLICATION NO.

MP0039.CIP

09/920,240

**LIST OF REFERENCES CITED BY APPLICANT**

APPLICANT

Pierte ROO

DATE SUBMITTED TO USPTO: July 12, 2005

FILING DATE

GROUP

08/01/2001

2682

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE
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	5,841,386	11/24/1998	Leduc			
	5,834,860	11/10/1998	Parsons, et al.			
	5,838,186	11/17/1998	Inoue, et al.			
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	5,589,788	12/31/1996	Goto			
	5,629,652	05/13/1997	Weiss			
	5,703,541	12/30/1997	Nakashima			
	5,719,515	02/17/1998	Danger			
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	6,046,607	04/04/2000	Kohdaka			
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JUL 12 2005

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*EXAMINER INITIALS	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION OR ABSTRACT
	63-300700	12/07/1988	Japan			
	06-97831	04/08/1994	Japan			
	06-97831	04/20/2005	Japan			Yes
	05-064231 A	03/12/1993	Japan			
	06-029853	02/04/1994	Japan			
	09-55770	08/17/1995	Japan			
	09-55770	08/17/1995	Japan			Yes
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	09-270707	04/19/2005	Japan			Yes
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	Young, et al., "A Low-Noise RF Voltage-Controlled Oscillator Using On-Chip High-Q Three-Dimensional Coil Inductor and Micromachined Variable Capacitor", June 8-11, 1998, pgs. 128-131.
	Young, et al., "A Micromachined Variable Capacitor for Monolithic Low-Noise VCOS", 1996, pgs. 86-89.
	Abidi, et al., "FA 7.2: The Future of CMOS Wireless Transceivers", February 7, 1997, pgs. 118-119, 440.
	Eto, et al., "A 333 MHz, 20mW, 18ps Resolution Digital DLL using Current-controlled Delay with Parallel Variables Resistor DAC (PVR-DAC)", August 28-30, 2000, pgs. 349-350.
	Harald, et al., "Design of a 10-bit 100 MSamples/s BiCMOS D/A Converter", 1996, pgs. 730-733.
	Lee, et al., "A 3V 10b 100MS/s DIGITAL-TO-ANALOG CONVERTER FOR CABLE MODEM APPLICATIONS", August 28-30, 2000, pgs. 203-205.
	Henriques, et al., "A CMOS Steering-Current Multiplying Digital-to-Analog Converter", 1995, pgs. 145-155.
	Wikner, et al., "Modeling of CMOS Digital-to-Analog Converters for Telecommunication", May, 1999, pgs. 489-499.
	Van der Plas, et al., "A 14-Bit Intrinsic Accuracy Q <sup>2</sup> Random Walk CMOS DAC", December, 1999, pgs. 1708-1718.
	Radke, et al., "A 14-Bit Current-Mode $\Sigma\Delta$ DAC Based Upon Rotated Data Weighted Averaging", August, 2000, pgs. 1074-1084.
	Shui, et al., "Mismatch Shaping for a Current-Mode Multibit Delta-Sigma DAC", March, 1999, pgs. 331-338.

EXAMINER

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	6,452,428 B1	09/17/2002	Mooney, et al.			
	5,745,564	04/28/1998	Meek			
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	5,838,177 (A)	11/17/1998	Keeth			
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	6,721,379 B1	04/13/2004	Cranford, Jr., et al.			
	5,859,552	01/12/1999	Do, et al.			

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OTHER DOCUMENTS (Including author, title, date, pertinent pages, etc.)

	Weaver, Jr., "A Third Method of Generation and Detection of Single-Sideband Signals," December 1956, pp. 1703-1705.
	Niknejad et al., "Analysis and Optimization of Monolithic Inductors and Transformers for RF ICs," 1997, pp. 375-378.
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	Gray et al., Analysis and Design of Analog Integrated Circuits, Fourth Edition.
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	Nguyen et al., "Si IC-Compatible Inductors and LC Passive Filters," August 1990, pp. 1028-1031.
	Gardner, "Charge-Pump Phase-Lock Loops," November 1980, pp. 1849-1858.
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	Dolle, "A Dynamic Line-Termination Circuit for Multireceiver Nets," December 1993, pp. 1370-1373.
	Su et al., "Experimental Results and Modeling Techniques for Substrate Noise in Mixed-Signal Integrated Circuits," April 1993, pp. 420-430.
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	Kim et al., "A 30-MHz Hybrid Analog/Digital Clock Recovery Circuit in 2- $\mu$ m CMOS," 1990, pp. 1385-1394.
	Liu et al., "WP 23.7: A 6.5 GHz Monolithic CMOS Voltage-Controlled Oscillator," 1999, pp. 404-405, 484.
	Wang et al., "WP 23.8: A 9.8 GHz Back-Gate Tuned VCO in 0.35 $\mu$ m CMOS," 1999, pp. 406-407, 484.
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	Dauphinee et al., "SP 23.7: A Balanced 1.5 GHz Voltage Controlled Oscillator with an Integrated LC Resonator," 1997, pp. 390-391, 491.
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	Chang et al., "A CMOS Channel-Select Filter for a Direct-Conversion Wireless Receiver," 1996, pp. 62-63.
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	Park et al., "A Low-Noise, 900-MHz VCO in 0.6 $\mu$ m CMOS," May 1999, pp. 586-591.
	Soyuer et al., "A Monolithic 2.3-Gb/s 100-mW Clock and Data Recovery Circuit in Silicon Bipolar Technology," December 1993, pp. 1310-1313.
	Hu et al., "A Monolithic 480 Mb/s Parallel AGC/Decision/Clock-Recovery Circuit in 1.2- $\mu$ m CMOS," December 1993, pp. 1314-1320.
	Parameswaran et al., "A New Approach for the Fabrication of Micromechanical Structures," December 6, 1998, pp. 289-307.
	Cho et al., "TP 13.5: A Single-Chip CMOS Direct-Conversion Transceiver for 900 MHz Spread-Spectrum Digital Cordless Phones," 1999, pp. 228-229, 464.

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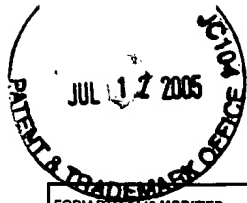
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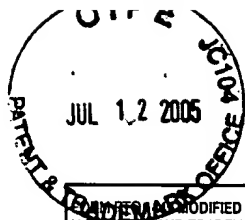
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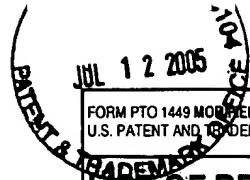
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2682

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EXAMINER

DATE CONSIDERED

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.